EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L7	3	((first adj ((phase adj locked adj loop) or pll) with fractional) and ((second adj ((phase adj locked adj loop) or pll) with integer))).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L8	4895	375/376	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L9	2	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and (wlan)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L10	295	(first adj ((phase adj locked adj loop) or pll)) same ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L11	0	pe3292	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L12	0	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) WITH SERIE and ((mobile adj station) or MS or (base adj station) OR CDMA)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L13	1	("2005/0062547").URPN.	USPAT	OR	ON	2007/09/26 13:51

L14	1740	(first adj ((phase adj locked adj loop) or pll)) and (second adj ((phase adj locked adj loop) or pll))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L15	200	transmit$4 and receiv$3 and (two adj dipole adj antenna)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L16	194	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L17	0	peredrine	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L18	4	(first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and (wlan)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L19	95	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and ((mobile adj station) or MS or (base adj station) or CDMA or wlan)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L20	398	(first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L21	453	peregrine	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L22	5	transmit$4 and receiv$3 and (two adj dipole adj antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L23	1	((first adj ((phase adj locked adj loop) or pll)) and ((second adj ((phase adj locked adj loop) or pll)) and fractional and (integer adj divider))).clm.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L24	629	(PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) same (fractional with (divider or synthesizer))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L25	564	(PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) with (fractional with (divider or synthesizer))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L26	1	WO99/08456	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L27	13	L8 and L19	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L28	3	(first adj ((phase adj locked adj loop) or pll)) same ((second adj ((phase adj locked adj loop) or pll)) and (VCO or (voltage adj controlled adj oscillator))) and (wlan)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L29	176	transmit$4 with receiv$3 and (two adj dipole adj antenna)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L30	325	internal adj antenna and TDMA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L31	797	(PLL or (phase adj locked adj loop)) and (integer with (divider or synthesizer)) and (fractional with (divider or synthesizer))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L32	169	L31 and L8	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L33	0	"10664850"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L34	111	"08456"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L35	75336	internal and antenna	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L36	3486	internal adj antenna	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L37	194	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L38	4	internal adj antenna and tranceiver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L39	92	transmit$4 same receiv$3 and (two with dipole with antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L40	1080	internal adj antenna and transceiver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L41	144	internal adj antenna with transceiver	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L42	94	transmit$4 and receiv$3 and (two with dipole with antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L43	5	transmit$4 same receiv$3 and (two adj dipole adj antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L44	564	((PLL or (phase adj locked adj loop))) and ((integer with (divider or synthesizer)) with (fractional with (divider or synthesizer)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L45	2	"6594508".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L46	2	"5838730".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L47	1	"10/396118"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:20
L48	1	"10/664850"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L49	5	transmit$4 with receiv$3 and (two adj dipole adj antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L50	10	("5038115" | "5072195" | "5302919" | "5374904" | "5382922" | "5414390" | "5559473" | "5579184" | "5657359" | "5734301").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/26 13:51
L51	13	peregrine and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L52	2	"6090648".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L53	2	"20060085662".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L54	3	"7003686".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L55	95	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and ((mobile adj station) or MS or (base adj station))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L56	2	"4720688".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L57	45	L8 and L10	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L58	2	"6181923".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L59	2	"5875186".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L60	2	pe3291	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L61	2	"6,489,818".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L62	65	(first adj synthesizer) and (second adj synthesizer) and ((phase adj locked adj loop) or pll)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L63	23	internal adj antenna and transceiver and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L64	3	internal adj antenna with transceiver and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L65	6	internal adj antenna same transceiver and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L66	2	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and (wlan)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L67	2	"99/08456"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L68	5	"9908456"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L69	2	internal adj antenna with TDMA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51

L70	7	("5107335" | "5423085" | "5432855" | "5436927" | "5633898" | "5727030" | "5768321").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/09/26 13:51
L71	8	internal adj antenna same TDMA	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L72	92	transmit$4 with receiv$3 and (two with dipole with antenna) and pll	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L73	5	(first adj ((phase adj locked adj loop) or pll)) with ((second adj ((phase adj locked adj loop) or pll)) with (VCO or (voltage adj controlled adj oscillator))) and mobile adj station	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L74	16	transmit$4 with receiv$3 with (two adj dipole adj antenna)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L75	45	(second adj (PLL or (phase adj locked adj loop))) and ((integer with (divider or synthesizer)) with (fractional with (divider or synthesizer)))	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 13:51
L76	3	transmit with receive with (two adj dipole adj antenna)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:12

L77	2	"6484038".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:14
L78	2	"6125268".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:15
L79	2	"5267189".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:15
L80	2	(dual near synthesizer and fractional and divider).clm.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:21
L81	24	(dual near synthesizer and fractional and divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:23
L82	4	(dual near synthesizer and fractional and integer near divider)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/09/26 14:21

Google

"dual output synthesizer" "integer divider"   [Search]   Advanced Search
Preferences

Web   Results **1 - 10** of about **43** for **"dual output synthesizer"**. (0.32 seconds)

Dual output synthesizer. (Full Spectrum).(Narda) Wireless Design ...
Dual output synthesizer. (Full Spectrum).(Narda) from Wireless Design & Development in
Array provided by LookSmart Find Articles.
findarticles.com/p/articles/mi_hb5077/is_200111/ai_n18456371 - 26k -
Cached - Similar pages - Note this

Dual output synthesizer.(Sources) Industry & Business Article ...
Goliath Industry and Business News includes thousands of articles from journals,
newspapers, newswires, and magazines, covering a wide range of industries ...
goliath.ecnext.com/coms2/gi_0199-1147105/**Dual-output-synthesizer**-Sources.html - 21k -
Cached - Similar pages - Note this

Dual output synthesizer.(Sources) - Journal, Magazine, Article ...
Dual output synthesizer.(Sources) Publication Date: 01-FEB-03 Publication Title:
Microwave Journal Format: Online - approximately 75 words Author: ...
goliath.ecnext.com/coms2/summary_0199-1147105_ITM - 27k -
Cached - Similar pages - Note this

Method and apparatus to generate signals using two or more phase ...
A wireless communication system comprising: a mobile station having a **dual output
synthesizer**, which includes: a first phase locked loop to set a frequency ...
www.freepatentsonline.com/20050062547.html - 32k - Cached - Similar pages - Note this

Frequency sources, Low-Noise Crystal Oscillator
HIGH PERFORMANCE Ku- AND L-BAND **DUAL OUTPUT SYNTHESIZER**. CFS SERIES:.
12.71–14.84 GHz. 12.46–13.28GHz. 10.91–11.53 GHz. 12.97–13.75 GHz. 11.46–12.03
GHz ...
miteq.com/micro/fresourc/c38/freqsyn/cfsfeatures.htm - 79k -
Cached - Similar pages - Note this

[PDF] SYNTHESIZER SECTION
File Format: PDF/Adobe Acrobat - View as HTML
HIGH PERFORMANCE Ku- AND L-BAND **DUAL OUTPUT SYNTHESIZER**. CFS SERIES:.
12.71–14.84 GHz. 10.91–11.53 GHz. 11.46–12.03 GHz. 12.46–13.28 GHz. 12.97–13.75
GHz ...
miteq.com/micro/fresourc/c38/pdfs/c38synth.pdf - Similar pages - Note this
[More results from miteq.com]

Evaluation modules for synthesizers chips
Valon 5001 **dual output synthesizer** module with Silicon Labs Si4133 includes trimmable
TCXO and voltage regulators. Typical modules available for 800-1800MHz ...
valontechnology.com/eval_synthzrs.html - 15k - Cached - Similar pages - Note this

TRAK Microwave Corporation - Core Technologies
Dual Output Synthesizer, 9 - 10 GHz, SYN147, Email · Quad Frequency Synthesizer, 0.65
- 0.95 HGz, SYN148, Email · IMA | Sources | Ferrites | Timing | Space ...
www.trak.com/CoreTechnologies.aspx?CoreTechnologyId=11 - 70k -
Cached - Similar pages - Note this

TRAK Microwave Corporation - Radar
Dual Output Synthesizer, 9 - 10 GHz, SYN147, Email · Quad Frequency Synthesizer, 0.65
- 0.95 HGz, SYN148, Email · + Frequency Sources Indirect ...
www.trak.com/radar.aspx - 114k - Cached - Similar pages - Note this
[More results from www.trak.com]

products ·
Programmable **dual-output synthesizer**. Narda introduces high-performance
programmable microwave synthesizers for wireless communications systems, ...
rfdesign.com/mag/radio_products_9/ - 64k - Cached - Similar pages - Note this

1 2 3 4   **Next**

Download Google Pack: free essential software for your PC

  ·"dual output synthesizer"   [Search]

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

Web   Images   Video   News   Maps   Gmail   more ▾

Google

"dual output synthesizer" "integer divider"   [Search]   Advanced Search
                                                            Preferences

---

Web   Results **1** - **1** of **1** for **"dual output synthesizer" "integer divider"**. (**0.16** seconds)

Tip: Try removing quotes from your search to get more results.

Method and apparatus to generate signals using two or more phase ...
... MS 120 may include a **dual output synthesizer** 130, a transceiver 140 and antennas ....
Integer divider 360 may provide a desired frequency to PLL 370. ...
www.freepatentsonline.com/20050062547.html - 32k - Cached - Similar pages - Note this

Download Google Pack: free essential software for your PC

"dual output synthesizer" "integer div[Search]

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

---

<u>Web</u>   <u>Images</u>   <u>Video</u>   <u>News</u>   <u>Maps</u>   <u>Gmail</u>   <u>more</u> ▾

drjatorres@gmail.com | <u>My Notebooks</u> | <u>Web History</u> | <u>My Account</u> | <u>Sign out</u>

<u>Google</u>

"dual output synthesizer" "fractional"   [Search]   <u>Advanced Search</u>
                                                            <u>Preferences</u>

Web   Results **1 - 2** of **2** for <u>**"dual output synthesizer" "fractional"**</u>. (**0.28** seconds)

Tip: Try removing quotes from your search to get more results.

<u>Method and apparatus to generate signals using two or more phase ...</u>
... **dual output synthesizer** 130 may include a **fractional** N synthesizer and a ... **fractional**
N synthesizer 310 may generate output signal 345 with the ...
www.freepatentsonline.com/20050062547.html - 32k - <u>Cached</u> - <u>Similar pages</u> - <u>Note this</u>

[PDF] <u>Untitled</u>
File Format: PDF/Adobe Acrobat
Dual Output Synthesizer, 9-10 GHz. SYN147. 100. Synthesizers ...... Alternative multi-loop
or **fractional**-n type designs do. improve the noise performance, ...
www.cornes-dodwell.co.jp/product/pdf/government.pdf - <u>Similar pages</u> - <u>Note this</u>

Download <u>Google Pack</u>: free essential software for your PC

"dual output synthesizer" "fractional"[Search]

<u>Search within results</u> | <u>Language Tools</u> | <u>Search Tips</u> | <u>Dissatisfied? Help us improve</u>

©2007 Google - <u>Google Home</u> - <u>Advertising Programs</u> - <u>Business Solutions</u> - <u>About Google</u>

Google

| "dual output synthesizer" "fractional" divider | Search | Advanced Search
Preferences |

Web   Results **1 - 2** of **2** for **"dual output synthesizer" "fractional" divider**. (**0.33** seconds)

Tip: Try removing quotes from your search to get more results.

Method and apparatus to generate signals using two or more phase ...

[0017] In some embodiments of the invention, **fractional** N synthesizer 310 may include a
fractional N **divider** 320, a phase locked loop (PLL) 330 and a ...
www.freepatentsonline.com/20050062547.html - 32k - Cached - Similar pages - Note this

[PDF] Untitled

File Format: PDF/Adobe Acrobat
Dual Output Synthesizer, 9-10 GHz. SYN147. 100. Synthesizers ...... Alternative multi-loop
or **fractional**-n type designs do. improve the noise performance, ...
www.cornes-dodwell.co.jp/product/pdf/government.pdf - Similar pages - Note this

Try Google Desktop: search your computer as easily as you search the web.

| "dual output synthesizer" "fractional" | Search |

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

---

SCIrUS
for scientific information only

View Career Opportunities

| About Us | Newsroom | Advisory Board | Submit Web Site | Help | | Contact Us |

Basic Search   Advanced Search   Search Preferences

"dual output synthesizer" AND "fractional" AND divider   [Search]

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for::   :All of the words:**"dual output synthesizer"** AND **"fractional"** AND **divider**

Found::   :**1 total | 0 journal results | 1 preferred web results | 0 other web results**

Sort by::   :**relevance | date**

[Save checked results]——[Email checked results]——[Export checked results]——•

Or

Al

☐ **1.** Method and apparatus to generate signals using two or more phase locked loops
Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Mar 2005
patno: US20050062547
...invention, **dual output synthesizer** 130 may include a **fractional** N synthesizer...may include a **fractional** N synthesizer...included in **dual output synthesizer** 300, if desired...this respect, **dual output synthesizer** 300 may include...
Full text available at patent office. For more in-depth searching go to ● LexisNexis™
similar results

F

∷fast

"dual output synthesizer" AND "fractional" AND divider   [Search]

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Downloads | Library Partners | Subscribe to News Updates | User Feedback
Advertising | Tell A Friend | Terms Of Service | Privacy Policy | Legal
Powered by FAST © Elsevier 2007

SCI**ſ**US
for scientific information only

| About Us | Newsroom | Advisory Board | Submit Web Site | Help | | Contact Us |

Basic Search   Advanced Search Search Preferences

"dual output synthesizer" AND fractional   [Search]

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for::   :All of the words:**"dual output synthesizer"** AND **fractional**

Found::   :**1 total** | **0 journal results** | **1 preferred web results** | **0 other web results**

Sort by::   :**relevance** | **date**

[Save checked results]———[Email checked results]———[Export checked results]—•

Or

☐ **1.** Method and apparatus to generate signals using two or more phase locked loops

Al

Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Mar 2005
patno: US20050062547

[F

...invention, **dual output synthesizer** 130 may include a **fractional** N synthesizer...include a **fractional** N synthesizer...included in **dual output synthesizer** 300, if desired...respect, **dual output synthesizer** 300 may include...frequency to **fractional** N synthesizer...
Full text available at patent office. For more in-depth searching go to ● LexisNexis⁻
similar results

∷*fast*

"dual output synthesizer" AND fractional   [Search]

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

SCIrUS
for scientific information only

Enhance Your Career

| About Us | Newsroom | Advisory Board | Submit Web Site | Help | | Contact Us |

Basic Search   Advanced Search Search Preferences

"dual output synthesizer" AND divider   [Search]

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for::   :All of the words:**"dual output synthesizer"** AND **divider**

Found::   :**1 total | 0 journal results | 1 preferred web results | 0 other web results**

Sort by::   :**relevance | date**

| Save checked results | Email checked results | Export checked results |

Or
Al

☐ **1.** Method and apparatus to generate signals using two or more phase locked loops
Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Mar 2005
patno:US20050062547
...200 may include a **dual output synthesizer** 300 and a transceiver...be included in
dual output synthesizer 300, if desired...in this respect, **dual output synthesizer** 300
may include...a fractional N **divider** 320, a phase locked...
Full text available at patent office. For more in-depth searching go to ● LexisNexis·
similar results

F

Buy
We
Key
ww·

Du;
Duε
ww·

::fast

"dual output synthesizer" AND divider   [Search]

☑ Journal sources   ☑ Preferred Web sources   ☑ Other Web sources   ☐ Exact phrase

SCIrUS
for scientific information only

Questions | **about** | **DVT?**

i n a t e

About Us | Newsroom | Advisory Board | Submit Web Site | Help | Contact Us

Basic Search　　　　　　　　　　　　　　　　Advanced Search　Search Preferences

"dual output synthesizer"　　　　　　　　　　　[Search]

☑ Journal sources · ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for:: 　:All of the words:**"dual output synthesizer"**

Found:: 　　:**3 total | 0 journal results | 2 preferred web results | 1 other web results**

Sort by:: 　　:**relevance | date**

[Save checked results]──[Email checked results]──[Export checked results]──➤
　　　　　　　　　　　　　　　　　　　　　　　　　　　　　　　　Or

☐ **1.** DISTRIBUTION OF DATA SIGNALS FROM BROADCAST DATA RECEIVING MEANS　　Al
STAFFORD, Gary (INVACOM LTD), *PATENT COOPERATION TREATY APPLICATION*, Aug
2007
patno: WO07096617　　　　　　　　　　　　　　　　　　　　　　　　　[F
The invention relates to a distribution network for broadcast data which is received at a
receiving means location. The network typically includes a plurality of cable connections
which carry the data in an RF or more typically an optical mode into which ...
Full text available at patent office. For more in-depth searching go to ◉ LexisNexis⁻
view all 2 results from Patent Offices
similar results

☐ **2.** (f) (e) (d) (c) (b) (a) (b) (c) (a) (e) (f) (d) (f) (d) (e) (b) (a)... [Word-2MB]
Aug 2003
...demodulator as possible. The filtered signal is amplifier and passed to the demodulator.
A frequency of 70 MHz from the **dual output synthesizer** is used by the demodulator to
separate the signal into the Q and I phase. 4.3 Calculation of System Specifications...
[http://www.stevens.edu/wireless/terrance/Final%20Repor...]
similar results

☐ **3.** Method and apparatus to generate signals using two or more phase locked loops
Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT
PUBLICATION*, Mar 2005
patno: US20050062547
...invention, MS 120 may include a **dual output synthesizer** 130, a transceiver 140
and...embodiments of the invention, **dual output synthesizer** 130 may include a
fractional...device 200 may include a **dual output synthesizer** 300 and a transceiver
250...
Full text available at patent office. For more in-depth searching go to ◉ LexisNexis⁻
view all 2 results from Patent Offices
similar results

Sponsored links

Musical Keyboards

Get Free Freight & Accessory Kit On Most Models Of Musical
Keyboards
www.andysmusiconline.com

Synthesizers
Wide Selection of Lab Supplies Free Shipping on Orders Over
$250
www.bestlabdeals.com

Dual Keyboard at Target
Find Dual Keyboard Online. Shop & Save at Target.com Today.
www.Target.com

"dual output synthesizer"   [Search]

☑ Journal sources   ☑ Preferred Web sources   ☑ Other Web sources   ☐ Exact phrase

IEEE *Xplore®*
RELEASE 2.4

Welcome United States Patent and Trademark Office

☐ **Search Results**

BROWSE   SEARCH   IEEE XPLORE GUIDE

Results for "((dual synthesizer)<in>metadata)"
Your search matched **3** of **1666250** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑ e-mail

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

| ((dual synthesizer)<in>metadata) | Search |

☐ Check to search only within this results set

Display Format: ⦿ Citation ⊂ Citation & Abstract

⌐ **view selected items**   <u>Select All</u> <u>Deselect All</u>

☐   1. **A 2.7 V GSM RF transceiver IC**
Irie, K.; Matsui, H.; Endo, T.; Watanabe, K.; Yamawaki, T.; Kokubo, M.; Hilders
<u>Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSC(
International</u>
6-8 Feb. 1997 Page(s):302 - 303, 475
Digital Object Identifier 10.1109/ISSCC.1997.585393

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(996 KB)   IEEE CNF
<u>Rights and Permissions</u>

☐   2. **An 18-mW 2.5-GHz/900-MHz BiCMOS dual frequency synthesizer with <1(
resolution**
Woogeun Rhee; Bisanti, B.; Ali, A.;
<u>Solid-State Circuits, IEEE Journal of</u>
Volume 37, <u>Issue 4</u>, April 2002 Page(s):515 - 520
Digital Object Identifier 10.1109/4.991389

<u>AbstractPlus</u> | <u>References</u> | Full Text: <u>PDF</u>(298 KB)   IEEE JNL
<u>Rights and Permissions</u>

☐   3. **An 18-mW 2.5-GHz/900-MHz BiCMOS dual frequency synthesizer with < 1
resolution**
Woogeun Rhee; Bisanti, B.; Ali, A.;
<u>Solid-State Circuits Conference, 2000. ESSCIRC '00. Proceedings of the 26th</u>
19-21 Sept. 2000 Page(s):252 - 255

<u>AbstractPlus</u> | Full Text: <u>PDF</u>(1112 KB)   IEEE CNF
<u>Rights and Permissions</u>

Help   Contact Us   Privacy & :

IEEE *Xplore*©
RELEASE 2.4

Welcome United States Patent and Trademark Office

□ Search Results

BROWSE   SEARCH   IEEE XPLORE GUIDE

Results for "((dual synthesizer and fractional)<in>metadata)"
Your search matched **2** of **1666250** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

☑ e-mail

» Search Options

View Session History

New Search

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

((dual synthesizer and fractional)<in>metadata)   **Search**

☐ Check to search only within this results set

Display Format: ⦿ Citation ⦾ Citation & Abstract

view selected items   **Select All Deselect All**

☐ 1. **An 18-mW 2.5-GHz/900-MHz BiCMOS dual frequency synthesizer with <1(
resolution**
Woogeun Rhee; Bisanti, B.; Ali, A.;
Solid-State Circuits, IEEE Journal of
Volume 37, Issue 4, April 2002 Page(s):515 - 520
Digital Object Identifier 10.1109/4.991389

AbstractPlus | References | Full Text: PDF(298 KB)   IEEE JNL
Rights and Permissions

☐ 2. **An 18-mW 2.5-GHz/900-MHz BiCMOS dual frequency synthesizer with < 1
resolution**
Woogeun Rhee; Bisanti, B.; Ali, A.;
Solid-State Circuits Conference, 2000. ESSCIRC '00. Proceedings of the 26th
19-21 Sept. 2000 Page(s):252 - 255

AbstractPlus | Full Text: PDF(1112 KB)   IEEE CNF
Rights and Permissions

Help   Contact Us ˙ Privacy & ˙

Indexed by
叼 Inspec©

IEEE *Xplore*®
RELEASE 2.4

Welcome United States Patent and Trademark Office

☐ **Search Results**

BROWSE   SEARCH   **IEEE XPLORE GUIDE**

Results for "((dual synthesizer and fractional and divider)<in>metadata)"   ✉ e-mail
Your search matched **0** documents.
A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» Search Options

<u>View Session History</u>

<u>New Search</u>

» Key

IEEE JNL	IEEE Journal or Magazine
IET JNL	IET Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IET CNF	IET Conference Proceeding
IEEE STD	IEEE Standard

Modify Search

| ((dual synthesizer and fractional and divider)<in>metadata) | **Search** |

☐ Check to search only within this results set

Display Format: ⊙ Citation   ○ Citation & Abstract

No results were found.

Please edit your search criteria and try again. Refer to the Help pages if you need assistan
search.

Help   Contact Us   Privacy & ·

Indexed by
🔲 **Inspec**°

Google

| "dual synthesizer" "fractional" divider | [Search] | Advanced Search |
| | | Preferences |

Web   Results **1 - 10** of about **82** for **"dual synthesizer" "fractional" divider**. (**0.32** seconds)

Method and apparatus to generate signals using two or more phase ...

[0017] In some embodiments of the invention, **fractional** N synthesizer 310 may include a
fractional N **divider** 320, a phase locked loop (PLL) 330 and a ...
www.freepatentsonline.com/20050062547.html - 32k - Cached - Similar pages - Note this

Tuning bandwidth minimization for low voltage dual band receiver ...

The Philips UM1005 and 8026 **dual synthesizer** integrated circuits available ... These
circuits include the use of **fractional-N dividers** and programmable loop ...
www.freepatentsonline.com/6125268.html - 93k - Cached - Similar pages - Note this
[More results from www.freepatentsonline.com]

[PDF] PRACTICAL DESIGN ASPECTS IN **FRACTIONAL**- N FREQUENCY SYNTHESIS

10 shows one example of the **dual synthesizer** block diagram [12]. The ..... B. Miller and R.
Conley, "A multiple modulator **fractional divider**," IEEE ...
www.springerlink.com/index/w8m145m747465110.pdf - Similar pages - Note this

[PDF] An 18-mW 2.5-GHz/900-MHz BiCMOS Dual Frequency Synthesizer with ...

Figure 4 shows the functional block diagram of the. **dual synthesizer**. As discussed
previously, the RF synthe-. sizer employs the **fractional-N** technique and ...
ieeexplore.ieee.org/iel5/9919/31528/01471259.pdf - Similar pages - Note this

[PDF] An 18-mW 2.5-GHz/900-MHz BiCMOS dual frequency synthesizer with ...

bined with the reference **divider** output and the main **divider**. output of each synthesizer.
III. E. XPERIMENTAL. R. ESULTS. A prototype **dual synthesizer** was ...
ieeexplore.ieee.org/iel5/4/21382/00991389.pdf - Similar pages - Note this

On-channel transceiver architecture in a dual band mobile phone ...

Dual loop frequency synthesizer having **fractional dividers** ... The Philips UM1005 and
8026 **dual synthesizer** integrated circuits available on the open market ...
www.patentstorm.us/patents/5890051-description.html - 105k -
Cached - Similar pages - Note this

[PDF] SA8026 2.5GHz low voltage **fractional–N** dual frequency synthesizer

File Format: PDF/Adobe Acrobat - View as HTML
2.5GHz low voltage **fractional–N** **dual synthesizer**. 2. 1997 Aug 07. FEATURES. •. Low
phase noise. •. Fully programmable main and auxiliary **dividers** ...
www.alphacron.de/download/hardware/SA8026.pdf - Similar pages - Note this

[PDF] SA7026 1.3GHz low voltage **fractional-N** dual frequency synthesizer

File Format: PDF/Adobe Acrobat - View as HTML
1.3GHz low voltage **fractional-N** **dual synthesizer**. 1999 Nov 04. 7. FUNCTIONAL
DESCRIPTION. Main **Fractional-N divider**. The RFin inputs drive a pre-amplifier ...
www.ortodoxism.ro/datasheets/philips/SA7026_4.pdf - Similar pages - Note this

[PDF] **Fractional/Integer-N PLL Basics**

File Format: PDF/Adobe Acrobat - View as HTML
divide by a fraction, the **fractional divider** value is achieved by averaging. ...... reference
sidebands better. Figure 29. **Dual Synthesizer** Using TRF2052 ...

Google

| "dual synthesizer" "fractional" "integer divider" | Search | Advanced Search Preferences |

Web   Results **1 - 3** of **3** for **"dual synthesizer" "fractional" "integer divider"**. **(0.26** seconds)

Tip: Try removing quotes from your search to get more results.

Method and apparatus to generate signals using two or more phase ...
In such embodiments, for example, the **fractional** N synthesizer may provide a signal with a
desired frequency to a first output of **dual synthesizer** 130. ...
www.freepatentsonline.com/20050062547.html - 32k - Cached - Similar pages - Note this

[PDF] ADI Wireless Seminar 2006 (Chapter V: Phase Locked Loops for High ...
File Format: PDF/Adobe Acrobat - View as HTML
In a **fractional**-N PLL, the N-divider. is broken up into the **integer divider** (N) and a.
modulus-M interpolator (M), which acts as the ...
www.analog.com/.../55371495239430ChapterV_
Phase_lockedLoopsForHigh_FrequencyReceiversAndTransmitters.pdf -
Similar pages - Note this

[PDF] SA8028 2.5 GHz sigma delta **fractional**-N / 760 MHz IF integer ...
File Format: PDF/Adobe Acrobat
is a **dual synthesizer** that integrates programmable dividers, ... **integer divider** bits
N<8:0> are in the B-word, whereas the **fractional** divider bits ...
www.nxp.com/acrobat_download/datasheets/SA8028_5.pdf - Similar pages - Note this

*In order to show you the most relevant results, we have omitted some entries very similar to
the 3 already displayed.*
If you like, you can repeat the search with the omitted results included.

Download Google Pack: free essential software for your PC

| "dual synthesizer" "fractional" "intege | Search |

Search within results | Language Tools | Search Tips | Dissatisfied? Help us improve

---

©2007 Google - Google Home - Advertising Programs - Business Solutions - About Google

focus.ti.com/lit/an/swra029/swra029.pdf - <u>Similar pages</u> - <u>Note this</u>

[PDF] <u>Evaluation and Performance of the SKY72300, SKY72301, SKY72302 ...</u>
File Format: PDF/Adobe Acrobat - <u>View as HTML</u>
The SKY74038 2.6 GHz/800 MHz, 21-bit **fractional**-N RF. main/integer-N IF auxiliary. Each
of these devices is a **dual synthesizer** (RF/IF) that offers low **...**
www.skyworksinc.com/products_display_item.asp?did=4712 - <u>Similar pages</u> - <u>Note this</u>

1 <u>2</u> <u>3</u> <u>4</u> <u>5</u> <u>6</u> <u>7</u>   **Next**

Try <u>Google Desktop</u>: search your computer as easily as you search the web.

"dual synthesizer" "fractional" divider [Search]

<u>Search within results</u> | <u>Language Tools</u> | <u>Search Tips</u> | <u>Dissatisfied? Help us improve</u>

©2007 Google - <u>Google Home</u> - <u>Advertising Programs</u> - <u>Business Solutions</u> - <u>About Google</u>

SCIſUS
for scientific information only

What's hot?

SCOPUS

About Us	Newsroom	Advisory Board	Submit Web Site	Help

Contact Us

Basic Search

Advanced Search   Search Preferences

"dual synthesizer" fractional "integer divider"   **Search**

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Searched for::   :All of the words:**"dual synthesizer"** AND **fractional** AND **"integer divider"**

Found::   :**1 total | 0 journal results | 1 preferred web results | 0 other web results**

Sort by::   :**relevance | date**

Save checked results	Email checked results	Export checked results

Or

☐ **1.** Method and apparatus to generate signals using two or more phase locked loops
Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Mar 2005
patno: US20050062547
...synthesizer 130 may include a **fractional** N synthesizer and a constant...embodiments, for example, the **fractional** N synthesizer may provide...frequency to a first output of **dual synthesizer** 130. In addition, the **fractional** N synthesizer may provide...
Full text available at patent office. For more in-depth searching go to ● LexisNexis⁻
similar results

Al

F

.::fast

"dual synthesizer" fractional "integer divider"   **Search**

☑ Journal sources ☑ Preferred Web sources ☑ Other Web sources ☐ Exact phrase

Downloads | Library Partners | Subscribe to News Updates | User Feedback
Advertising | Tell A Friend | Terms Of Service | Privacy Policy | Legal
Powered by FAST © Elsevier 2007

SCIrUS
for scientific information only

Scopus can help you to...   Check out who is being cited
Find. check and track citations
Track citations of a particular author
Track citations in a particular subject area

SCOPUS
refine your research

| About Us | Newsroom | Advisory Board | Submit Web Site | Help | | Contact Us |

Basic Search   Advanced Search   Search Preferences

"dual synthesizer" fractional integer divider   [Search]

☑ Journal sources   ☑ Preferred Web sources   ☑ Other Web sources   ☐ Exact phrase

Searched for::   :All of the words:**"dual synthesizer"** AND **fractional** AND **integer** AND **divider**

Found::   :**19 total** | **0 journal results** | **17 preferred web results** | **2 other web results**

Sort by::   :**relevance** | **date**

[Save checked results]———[Email checked results]———[Export checked results]——●

☐ **1.** Method and apparatus to generate signals using two or more phase locked loops
Reuven, Yossi, *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION,* Mar 2005
patno: US20050062547
...synthesizer 130 may include a **fractional** N synthesizer and a constant...to a first output of **dual synthesizer** 130. In addition, the...fundamental frequency for **integer** division synthesizer...embodiments of the invention, **fractional** N synthesizer 310 may include a **fractional** N **divider** 320, a phase locked loop...In this embodiment, **fractional** N **divider** 320 may divide a frequency...rational fraction of **integers** M and L, i.e., N=M/L...
Full text available at patent office. For more in-depth searching go to ● LexisNexis·
view all 16 results from Patent Offices
similar results

☐ **2.** TRANSCEIVER ARCHITECTURE IN A DUAL BAND MOBILE PHONE
SCHLANG, Jeffrey / BEOSCH, Ron / GORE, Charles / HADJICHRISTOS, Aristotle (Ericsson Inc.), *EUROPEAN PATENT,* Sep 2000
patno: EP1032982
...otherwise relate the reference **dividers** of the two PLLs so that...Philips UM1005 and 8026 **dual synthesizer** integrated circuits available...circuits include the use of **fractional**-N **dividers** and programmable loop bandwidth...by dividing by a smaller **integer,** and then interpolate between...using the technique of **fractional**-N synthesis . described in...
Full text available at patent office. For more in-depth searching go to ● LexisNexis·
view all 16 results from Patent Offices
similar results

☐ **3.** TUNING BANDWIDTH MINIMIZATION FOR LOW VOLTAGE DUAL BAND RECEIVER
BOESCH, Ronald D. / DOLMAN, Rodney A. / GORE, Charles / DENT, Paul W. (Ericsson Inc.), *EUROPEAN PATENT,* Sep 2000
patno: EP1032981
...relate the reference **dividers** of the two PLLs so...Philips UM1005 and 8026 **dual synthesizer** integrated circuits...include the use of **fractional**-N **dividers** and programmable loop...to two, one feeding **dual synthesizer** circuit (14 + 17...smallest possible **integer** for comparison with...

Re
us
fou
cor
cor
cry
cry
err
fre
fre
int·
out
out
pha
pha
ref
sig
tra
tur
Or
Al

[F

Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **4.** SIMPLIFIED REFERENCE FREQUENCY DISTRIBUTION IN A MOBILE PHONE
GORE, Charles / DOLMAN, Rodney A. / DENT, Paul W. (Ericsson Inc.), *EUROPEAN PATENT*, Sep 2000
patno: EP1032972
...relate the reference **dividers** of the two PLLs so...Philips UM1005 and 8026 **dual synthesizer** integrated circuits...include the use of **fractional**-N **dividers** and programmable loop...to two, one feeding **dual synthesizer** circuit (14+17) and...smallest possible **integer** for comparison with...
Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **5.** Method and apparatus for generating a plurality of reference frequencies in a mobile phone using a common crystal reference oscillator
Gore, Charles / Dolman, Rodney A. / Dent, Paul W. (Ericsson Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Nov 2002
patno: US6484038
...LO to a programmable **integer** multiple of a basic tuning...using the technique of **fractional**-N synthesis described...reduced to two, one feeding **dual synthesizer** circuit (14+17) and the...N3 in third variable **divider** (48), to produce a frequency...Preferably, N3 is not an **integer** factor but comprises a whole part and a **fractional** part, the components...phase reference from **divider** (41) to produce a frequency...45,46,47) also to be a **fractional**-N synthesizer, however...are more complex than **integer** synthesizers and thus...
Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **6.** HIGH AGILITY FREQUENCY SYNTHESIZER PHASE-LOCKED LOOP
AKBAR, Ali / YOUNG, James P. (SKYWORKS SOLUTIONS, INC.), *PATENT COOPERATION TREATY APPLICATION*, Nov 2005
patno: WO05104385
...as for example a frequency **divider** or multiplier. In addition...second frequency are non- **integer** multiples to reduce cross-coupling...frequencies that are non-**integer** multiples. Thus, the two...an example embodiment of a **dual synthesizer** signal generator. Figure...an example embodiment of a **divider** or multiplier circuit. Figure...
Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **7.** TRANSCEIVER ARCHITECTURE IN A DUAL BAND MOBILE PHONE
SCHLANG, Jeffrey / BEOSCH, Ron / GORE, Charles / HADJICHRISTOS, Aristotle (ERICSSON, INC.), *PATENT COOPERATION TREATY APPLICATION*, May 1999
patno: WO9926353
...otherwise relate the reference **dividers** of the two PLLs so that...Philips UM1005 and 8026 **dual synthesizer** integrated circuits available...circuits include the use of **fractional**-N **dividers** and programmable loop bandwidth...by dividing by a smaller **integer**, and then interpolate between...using the technique of **fractional**-N synthesis described in...
Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **8.** TUNING BANDWIDTH MINIMIZATION FOR LOW VOLTAGE DUAL BAND RECEIVER
BOESCH, Ronald D. / DOLMAN, Rodney A. / GORE, Charles / DENT, Paul W.

(ERICSSON INC.), *PATENT COOPERATION TREATY APPLICATION*, May 1999
patno: WO9926352
...relate the reference **dividers** of the two PLLs so...Philips UM1005 and 8026 **dual synthesizer** integrated circuits...include the use of **fractional**-N **dividers** and programmable loop...to two, one feeding **dual synthesizer** circuit (14 + 17...smallest possible **integer** for comparison with...
Full text available at patent office. For more in-depth searching go to ⬤ LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **9.** SIMPLIFIED REFERENCE FREQUENCY DISTRIBUTION IN A MOBILE PHONE
GORE, Charles / DOLMAN, Rodney A. / DENT, Paul W. (ERICSSON, INC.), *PATENT COOPERATION TREATY APPLICATION*, May 1999
patno: WO9926336
...relate the reference **dividers** of the two PLLs so...Philips UM1005 and 8026 **dual synthesizer** integrated circuits...include the use of **fractional**-N **dividers** and programmable...oscillator frequency by an **integer** factor. Since according...two, one feeding **dual synthesizer** circuit (14+17) and...smallest possible **integer** for comparison with...
Full text available at patent office. For more in-depth searching go to ⬤ LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **10.** An agile frequency synthesizer for frequency hopping radio
Shenouda, Hany H., *dissertation*, Jan 2000
...noise are given. A brief introductionto **Fractional**-NSynthesizerismade. Finally, a brief...2.1.4 Frequency **Divider** 14 ....2.2 Analysis of...2.3 **Fractional**-N Synthesis .... 24 2...Referencefrequency fo Center frequency N Reference **Divider** G(s) Loop FilterTransfer Function 'I...
[http://hdl.handle.net/1880/39277]
similar results

☐ **11.** Simplified reference frequency distribution in a mobile phone
Gore, Charles / Dolman, Rodney A. / Dent, Paul W. (Ericsson Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Oct 2001
patno: US6308048
...LO to a programmable **integer** multiple of a basic tuning...using the technique of **fractional**-N synthesis described...reduced to two, one feeding **dual synthesizer** circuit (14+17) and the...N3 in third variable **divider** (48), to produce a frequency...Preferably, N3 is not an **integer** factor but comprises a whole part and a **fractional** part, the components...phase reference from **divider** (41) to produce a frequency...45,46,47) also to be a **fractional**-N synthesizer, however...are more complex than **integer** synthesizers and thus...
Full text available at patent office. For more in-depth searching go to ⬤ LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **12.** HP 8360 Series Synthesized Sweepers (Including Options 001, 003, 004, 006, and 008) User's Handbook [PDF-6MB]
Sep 2001
HP 8360 Series Synthesized Sweepers (Including Options 001, 003, 004, 006, and 008) User's Handbook SERIAL NUMBERS This manual applies directly to any synthesized sweeper with serial number prefix combinations.
[http://cfa-www.harvard.edu/~thunter/manuals/EPSG087108...]
more hits from [http://cfa-www.harvard.edu]
similar results

☐ **13.** Tuning bandwidth minimization for low voltage dual band receiver
Boesch, Ronald D. / Dolman, Rodney A. (Ericsson Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Sep 2000

patno:US6125268

...1st LO to a programmable **integer** multiple of a basic tuning...using the technique of **fractional**-N synthesis described in...reduced to two, one feeding **dual synthesizer** circuit (14+17) and the...frequency at the output of **divider** (41). The phase comparison...factor N3 in third variable **divider** (48), to produce a frequency...Preferably, N3 is not an **integer** factor but comprises a whole part and a **fractional** part, the components (48...

Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis·
· view all 16 results from Patent Offices
similar results

☐ **14.** On-channel transceiver architecture in a dual band mobile phone
Schlang, Jeffrey / Boesch, Ron / Gore, Charles / Hadjichristos, Aristotle (Ericsson Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Mar 1999
patno:US5890051

...1st LO to a programmable **integer** multiple of a basic tuning...using the technique of **fractional**-N synthesis described in...reduced to two, one feeding **dual synthesizer** circuit (14+17) and the...frequency at the output of **divider** (41). The phase comparison...factor N3 in third variable **divider** (48), to produce a frequency...Preferably, N3 is not an **integer** factor but comprises a whole part and a **fractional** part, the components (48...

Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis·
view all 16 results from Patent Offices
similar results

☐ **15.** High agility frequency synthesizer phase-locked loop
Ali, Akbar / Young, James P., *UNITED STATES PATENT AND TRADEMARK OFFICE PRE-GRANT PUBLICATION*, Oct 2005
patno:US20050227629

...a frequency multiplier, a frequency **divider**, or both configured to modify the frequency...frequencies that are related by non-**integer** multiples. For example, the ratio 1...phase detector, frequency multiplier or **divider**, or charge pumps. It is also contemplated...508 may comprise pre-scalers and/or **fractional** modulators. It is contemplated that...

Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis·
view all 16 results from Patent Offices
similar results

☐ **16.** Rational fraction synthesizer
Wilke, William G., *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Nov 1993
patno:US5267189

...applied to input terminals of a 1/M programmable **divider** 72. **Divider** means 72 can be programmed by control lines 87, to divide by a range of **integers**, from M.sub.min to M.sub.max, and, like **divider** 74, can be any form of binary, decimal, straight...

Full text available at patent office. For more in-depth searching go to 🌐 LexisNexis·
view all 16 results from Patent Offices
similar results

☐ **17.** REDUCING OR ELIMINATING RADIO TRANSMITTER MIXER SPURIOUS OUTPUTS
CAIRNS, Steven G. (ERICSSON INC.), *PATENT COOPERATION TREATY APPLICATION*, Sep 1997
patno:WO9735381

...on a control bit input. The output of **divider** 1] 2 is fed to one IIIPLIt Of phase detector...the XT.AL_I InpLit) Is divided bN, a **divider** 114 that proo-Tammably divides by 8 or...108 and "N" is the division ratio of **divider** 110. The output of WO 104 is applied...M * TXLO (2) spur where N and M are **integer** multipliers and N + M is the -order...

Full text available at patent office. For more in-depth searching go to LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **18.** Reducing or eliminating radio transmitter mixer spurious outputs
Cairns, Steven G. (Ericsson Inc.), *UNITED STATES PATENT AND TRADEMARK OFFICE GRANTED PATENT*, Aug 1998
patno:US5794131
...optional internal **divider** or optional prescaler...of a Philips SA7025 **dual synthesizer** chip). In this example...206b of the SA7025 **dual synthesizer** chip and associated...includes a dual output **fractional** N type phase detector...
Full text available at patent office. For more in-depth searching go to LexisNexis⁻
view all 16 results from Patent Offices
similar results

☐ **19.** MBRM120ET3 (3) VIEW [PDF-150K]
Sep 2000
The advanced packaging techniques provide for a highly efficient micro miniature, space saving surface mount Rectifier.
[http://cfa-www.harvard.edu/~thunter/datasheets/mbrm120...]
similar results

Sponsored links

RF System Design Notes
200 RF Wireless & Cable Application Circuits & Tutorials.
Download free
maxim-ic.com/appnote

::*fast*

"dual synthesizer" fractional integer divider	Search

☑ Journal sources   ☑ Preferred Web sources   ☑ Other Web sources   ☐ Exact phrase

PALM Intranet

Application Number [   ] [Submit]

IDS Flag Clearance for Application 10664850

IDS Information

Content	Mailroom Date	Entry Number	IDS Review	Last Modified	Reviewer
WIDS	2003-09-22	11	Y ☑	2007-05-09 00:00:00.0	CR #232884
[Update]					

PALM INTRANET

Continuity Information for 10/664850

Parent Data
No Parent Data

Child Data
No Child Data

| Appln Info | Contents | Petition Info | Atty/Agent Info | **Continuity/Reexam** | Foreign Data |

Search Another: Application # _____ **or Patent#** _____ [Search]
    [Search]

PCT / [____] / [____] [Search] **or PG PUBS #** [_____]
    [Search]

Attorney Docket #   [Search]
Bar Code # [_____] [Search]

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Foreign Information for 10/664850

No Foreign Data

| Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign Data |

Search Another: Application
Search

or Patent#   Search

PCT /   /   Search

or PG PUBS #
Search

Attorney Docket # _____   Search

Bar Code # _____   Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Inventor Information for 10/664850

Inventor Name	City	State/Country
REUVEN, YOSSI	HADERA	ISRAEL

Appln Info | Contents | Petition Info | Atty/Agent Info | Continuity/Reexam | Foreign [

Search Another: Application #   [Search]   **or Patent#**   [Search]

PCT /   /   [Search]   **or PG PUBS #**   [Search]

Attorney Docket #   [Search]

Bar Code #   [Search]

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = REUVEN
First Name = YOSSI

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10664850	Not Issued	71	09/22/2003	Method and apparatus to generate signals using two or more phase locked loops	REUVEN, YOSSI
60641398	Not Issued	159	01/04/2005	Wired networking	REUVEN, YOSSI
60659913	Not Issued	159	03/10/2005	Access network	REUVEN, YOSSI
60682820	Not Issued	159	05/20/2005	Multi drop DSL	REUVEN, YOSSI
60688711	Not Issued	159	06/09/2005	DSL over coax	REUVEN, YOSSI

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
REUVEN	YOSSI	Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page

PALM INTRANET

Correspondence Address for 10/664850

Customer Number	Contact Information	Address
49444 Delivery Mode: PAPER	Telephone: (646)878-0800 Fax: (646)878-0801 E-Mail: PUSDKT@PCZLAW.COM	PEARL COHEN ZEDEK LATZER, LLP 1500 BROADWAY, 12TH FLOOR NEW YORK NY 10036

[Appln Info] [Contents] [Petition Info] [Atty/Agent Info] [Continuity/Reexam] [Foreign [

Search Another: Application # _____   **or Patent#** _____ [Search]
[Search]

PCT / _____ / _____ [Search]   **or PG PUBS #** _____
[Search]

Attorney Docket #   [Search]

Bar Code #   [Search]

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page